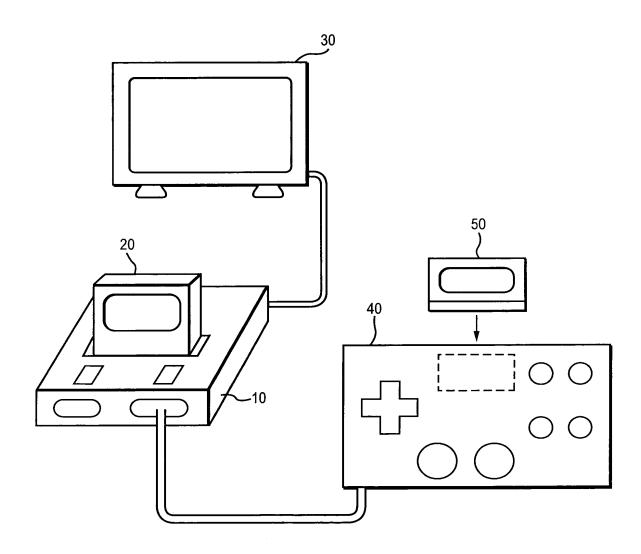


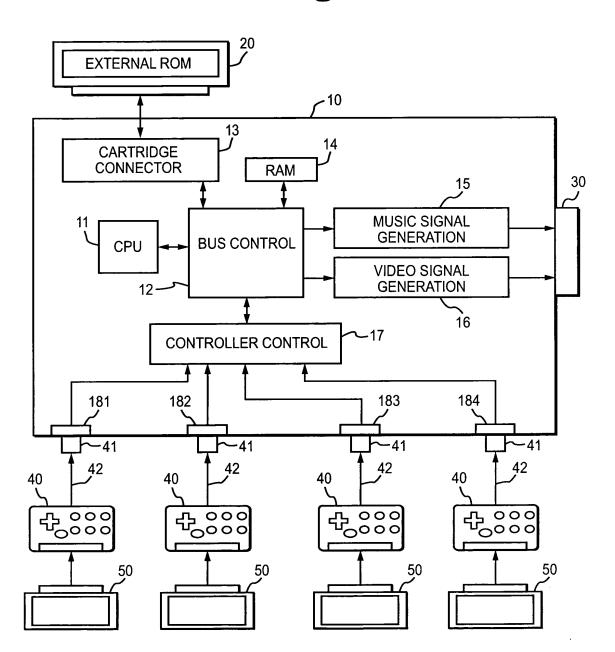
Inventor: NISHIUMI et al SN 09/686,761/Sheet 1 of 13 Atty. Dkt.: 723-933

Fig. 1



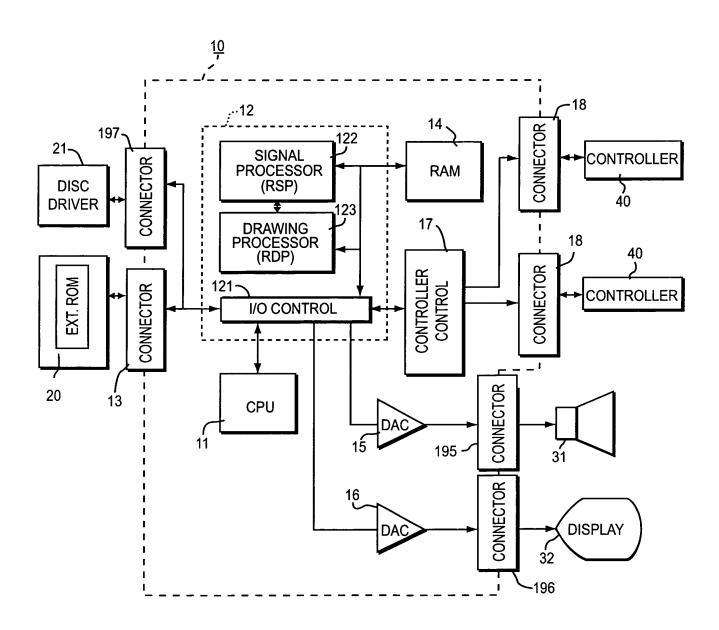
Inventor: NISHIUMI et al SN 09/686,761/Sheet 2 of 13 Atty. Dkt.: 723-933

Fig. 2



Inventor: NISHIUMI et al SN 09/686,761/Sheet 3 of 13

Fig. 3

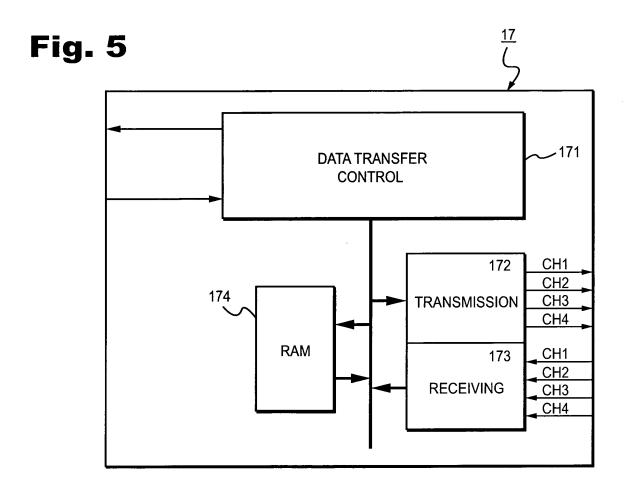


Inventor: NISHIUMI et al SN 09/686,761/Sheet 4 of 13 Atty. Dkt.: 723-933

Fig. 4

	RAM 14
<u>201</u>	IMAGE DATA REGION
<u>202</u>	PROGRAM DATA REGION
202	
<u>141</u>	CONTROLLER DATA REGION
<u>142</u>	SPEED DATA REGION

Inventor: NISHIUMI et al SN 09/686,761/Sheet 5 of 13 Atty. Dkt.: 723-933



Inventor: NISHIUMI et al SN 09/686,761/Sheet 6 of 13 Atty. Dkt.: 723-933

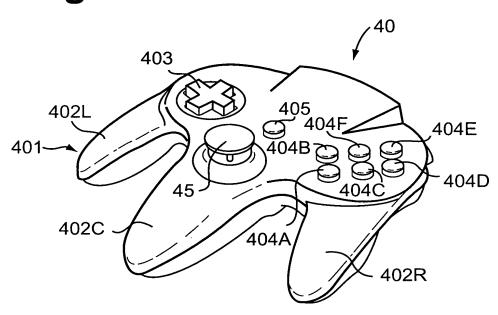
Fig. 6

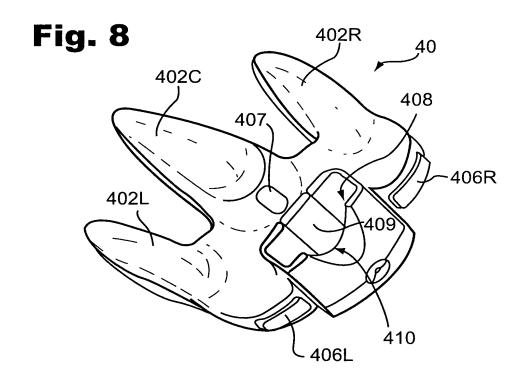
RAM 174

RAM 174		
1CH COMMAND STORAG POSITION	E 174a	
1		SMISSION/RECEPTION TORAGE POSITION
174b		
2CH COMMAND STORAG POSITION	E 174c	
2 174d		SMISSION/RECEPTION TORAGE POSITION
3CH COMMAND STORAG POSITION	E 174e	
3		SMISSION/RECEPTION TORAGE POSITION
174f		
4CH COMMAND STORAG POSITION	E 174g	
4		SMISSION/RECEPTION FORAGE POSITION
174h		

Inventor: NISHIUMI et al SN 09/686,761/Sheet 7 of 13 Atty. Dkt.: 723-933

Fig. 7





Inventor: NISHIUMI et al SN 09/686,761/Sheet 8 of 13 Atty. Dkt.: 723-933

Fig. 9 40 2-SHAFT PULSE COUNTER 444 ,444Y 442 43 45 COUNTER COUNTER 42 445 444X TRANS-**CONVERSION RESET MITTING** 448 **POWER** ON **COMMAND** RECEIVING CONTROL RESET RESET **RESET PIN** SW RESET 447 441 443 LEFT—ORIGHT—OSTART—O SW RESET SW SIGNAL DETECTION 403 - 407 JOYPORT CONTROL 446 <u>50</u> RD/WR/CS DATA **ADDRESS** RAM -52 51

Inventor: NISHIUMI et al SN 09/686,761/Sheet 9 of 13

Fig. 10

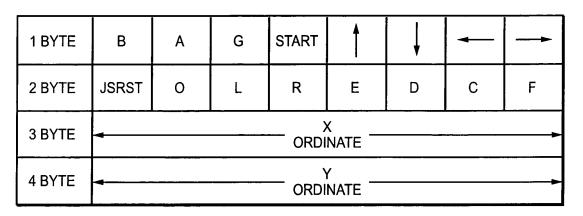
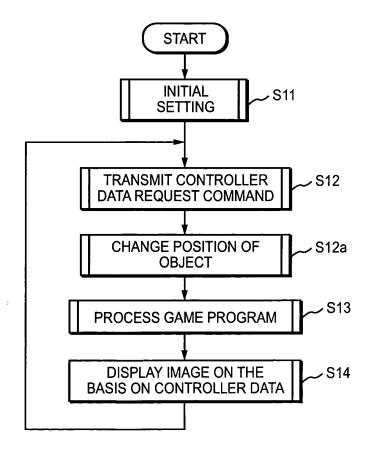
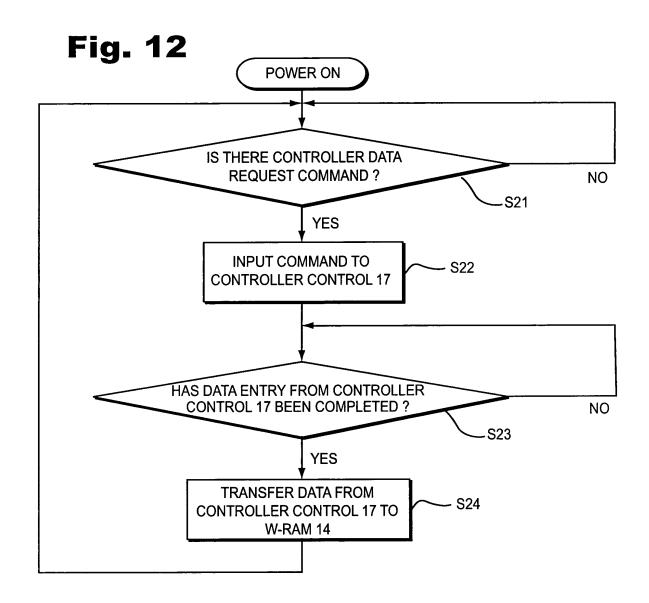


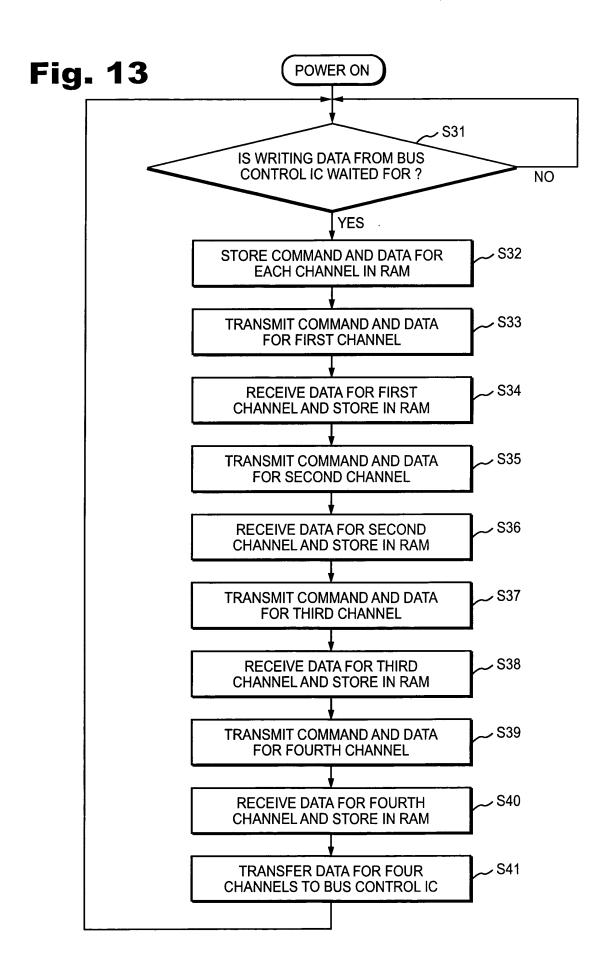
Fig. 11



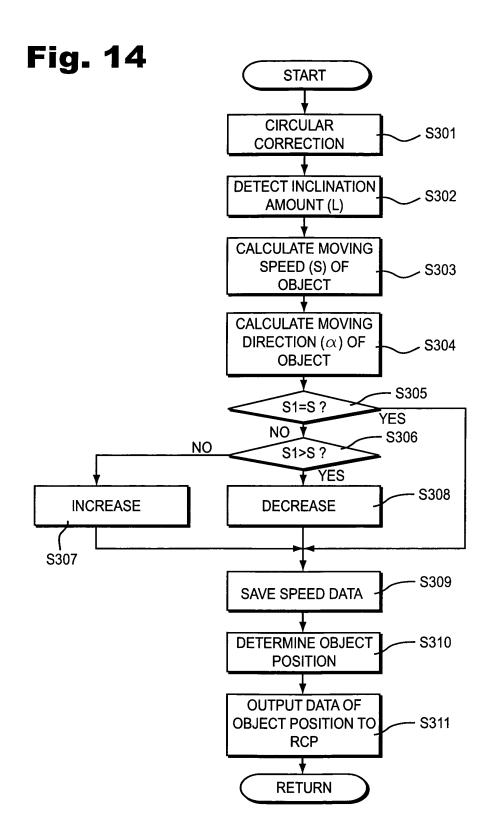
Inventor: NISHIUMI et al SN 09/686,761/Sheet 10 of 13



Inventor: NISHIUMI et al SN 09/686,761/Sheet 11 of 13



Inventor: NISHIUMI et al SN 09/686,761/Sheet 12 of 13



Inventor: NISHIUMI et al SN 09/686,761/Sheet 13 of 13 Atty. Dkt.: 723-933

